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1 Research Interests

Architectures, methodologies, software techniques, and tools for design of signal processing systems.

2 Education

- Ph.D. in Electrical Engineering and Computer Sciences, University of California at Berkeley, 1994
- M.S. in Electrical Engineering and Computer Sciences, University of California at Berkeley, 1991
- B.S. in Electrical and Computer Engineering, and Computer Science (double major) University of Wisconsin at Madison, 1987

3 Academic Positions

- 2006–present: Professor, Dept. of Electrical and Computer Engineering, University of Maryland, College Park, MD (UMD).
- 1997–present: Joint Faculty Appointment, Institute for Advanced Computer Studies (UMI-ACS), UMD.
- 2001–2006: Associate Professor, Dept. of Electrical and Computer Engineering, UMD.
- 1997–2001: Assistant Professor, Dept. of Electrical and Computer Engineering, UMD.
- 1992–1994: Research Assistant, Electronics Research Laboratory, University of California, Berkeley, CA.
- 1987–1991: Research Assistant, Electronics Research Laboratory, University of California, Berkeley, CA.

4 Industrial Positions

- 1994–1997: Researcher, Semiconductor Research Laboratory, Hitachi America, San Jose, CA.
- 1991–1992: Compiler Developer, Kuck & Associates, Champaign, IL.

5 Honors

- IEEE Fellow (2011)
- Fulbright Senior Specialist (Austria, 2009 and Germany, 2005)
- Nokia Distinguished Lecturer (Finland, 2006)
- National Science Foundation CAREER Award (1998)

6 Books

- S. S. Bhattacharyya, E. Deprettere, R. Leupers, and J. Takala, editors. *Handbook of Signal Processing Systems*. Springer, 2010.
- S. Sriram and S. S. Bhattacharyya. *Embedded Multiprocessors: Scheduling and Synchronization*. CRC Press, second edition, 2009; Marcel Dekker, first edition, 2000.
- B. Kisanin, S. S. Bhattacharyya, and S. Chai, editors. *Embedded Computer Vision*. Springer, 2008.
- P. K. Murthy and S. S. Bhattacharyya. *Memory Management for Synthesis of DSP Software*. CRC Press, 2006.
- S. S. Bhattacharyya, E. Deprettere, and J. Teich, editors. *Domain-Specific Processors: Systems, Architectures, Modeling, and Simulation*. Marcel Dekker, 2003.
- S. S. Bhattacharyya, P. K. Murthy, and E. A. Lee. *Software Synthesis from Dataflow Graphs*. Kluwer Academic Publishers, 1996.

7 Other Publications

Author or co-author of over 150 refereed technical articles. More details are available at <http://dsp.serv.eng.umd.edu/pub/dspcad/papers/CONTENTS.html>, and <http://www.ece.umd.edu/~ssb/ssb-cv.pdf>.

8 Document Version

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